

FIG. 1

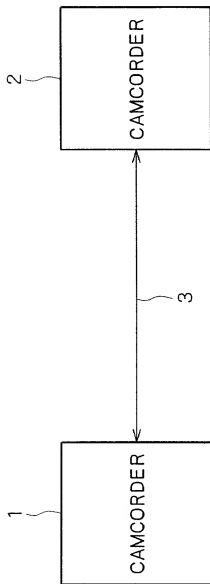


FIG. 2

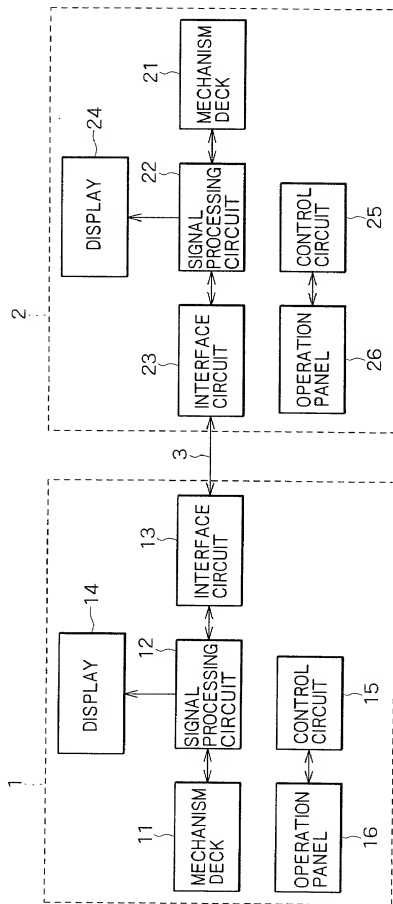


FIG. 3

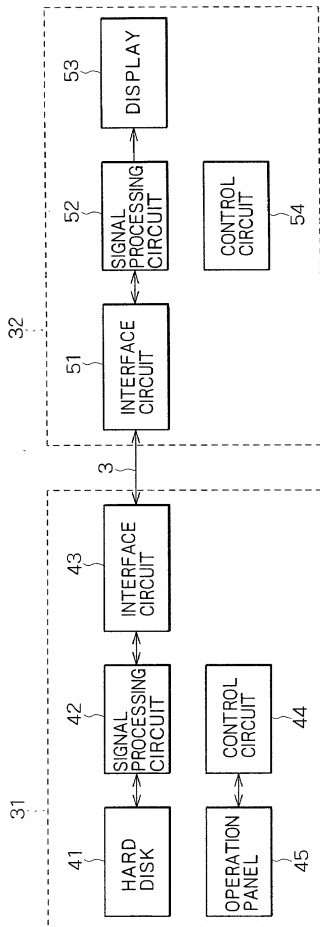


FIG. 4

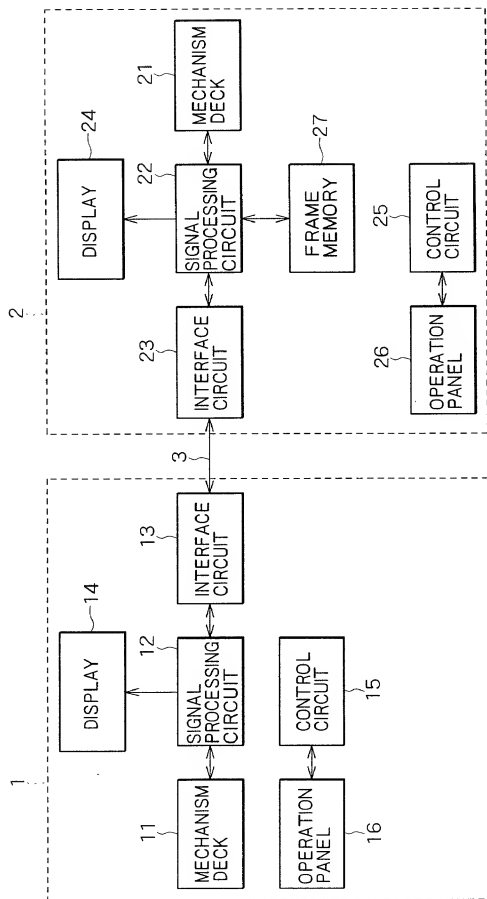
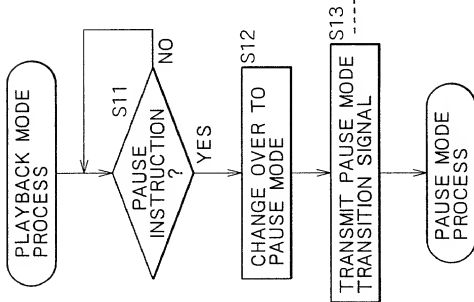
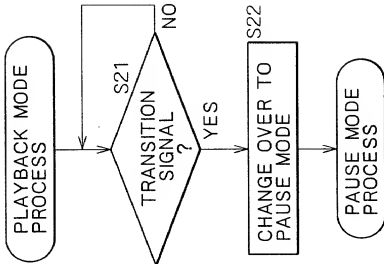


FIG. 6A



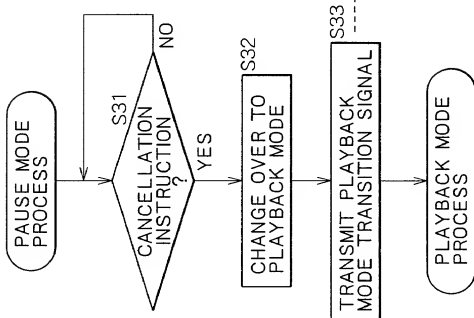
PROCESS OF CONTROL
CIRCUIT 15

FIG. 6B



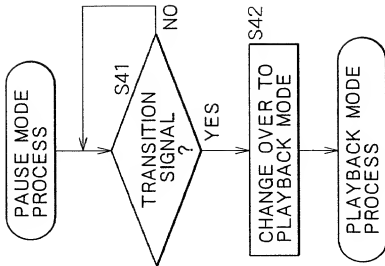
PROCESS OF CONTROL
CIRCUIT 25

FIG. 7A



PROCESS OF CONTROL
CIRCUIT 15

FIG. 7B



PROCESS OF CONTROL
CIRCUIT 25

FIG. 8

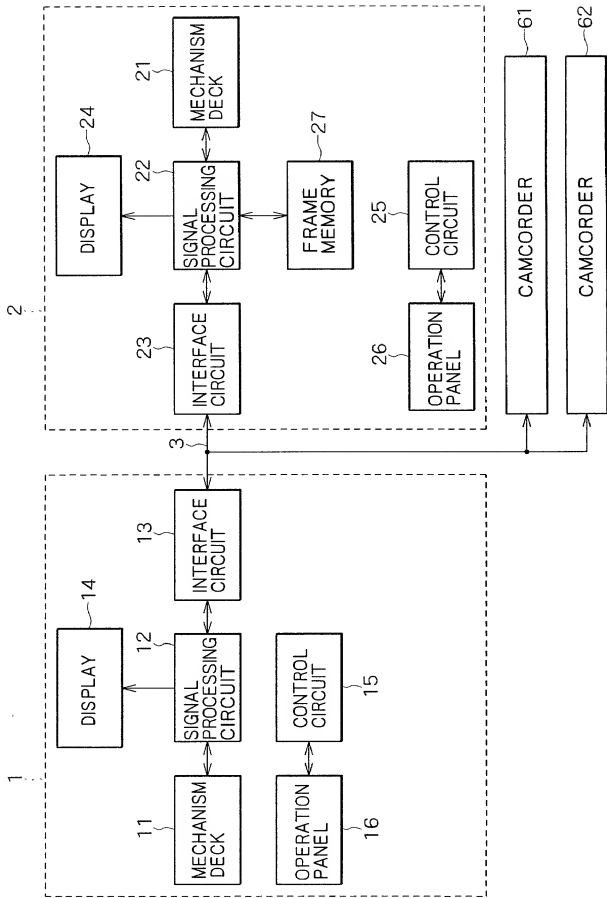


FIG. 9

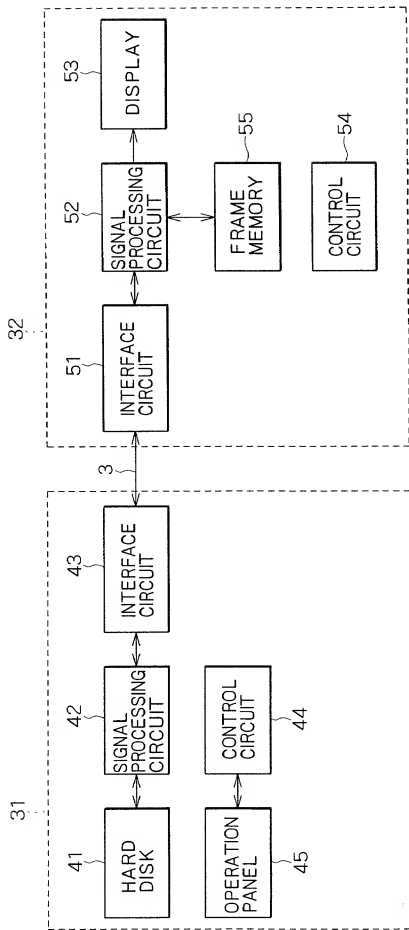
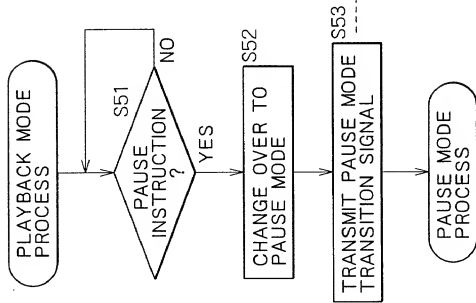
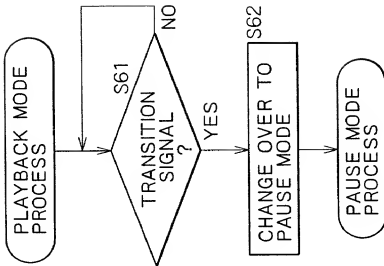


FIG. 10A



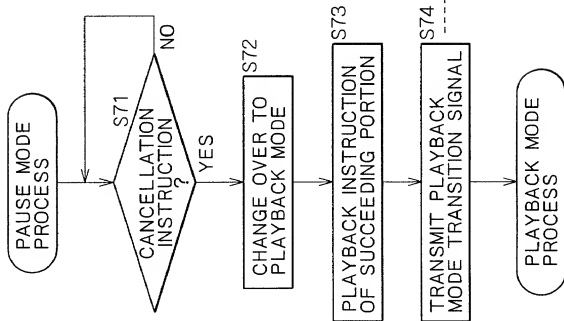
PROCESS OF CONTROL
CIRCUIT 44

FIG. 10B



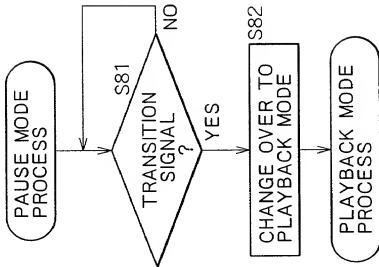
PROCESS OF CONTROL
CIRCUIT 54

FIG. 11A



PROCESS OF CONTROL
CIRCUIT 44

FIG. 11B



PROCESS OF CONTROL
CIRCUIT 54

FIG. 12

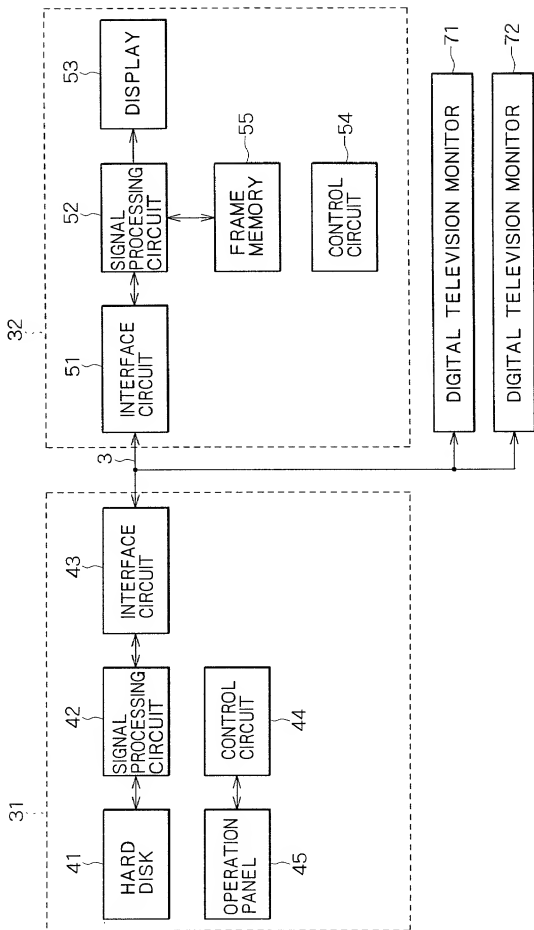


FIG. 13

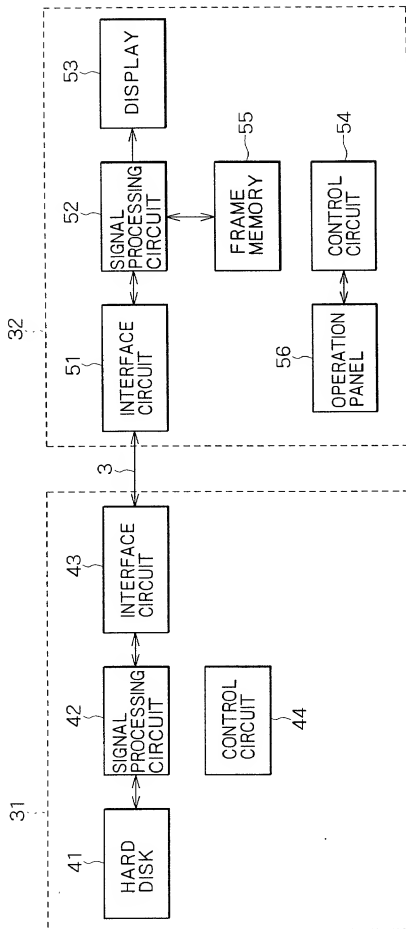


FIG. 14A

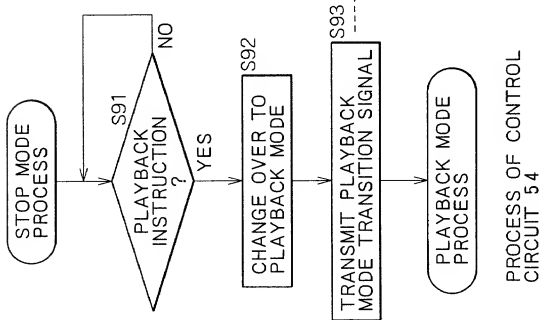


FIG. 14B

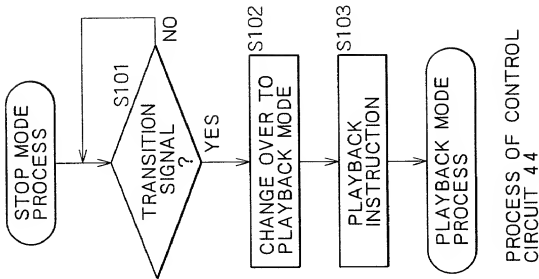
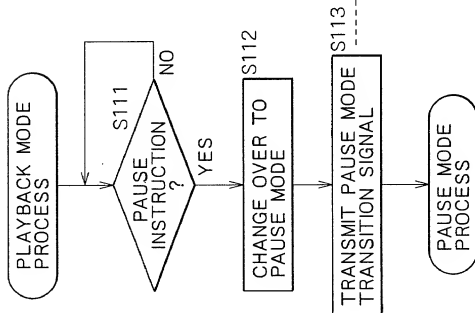
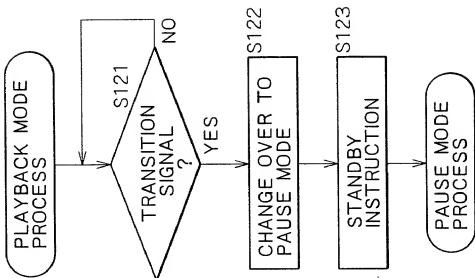


FIG. 15A



PROCESS OF CONTROL
CIRCUIT 54

FIG. 15B



PROCESS OF CONTROL
CIRCUIT 44

FIG. 16A

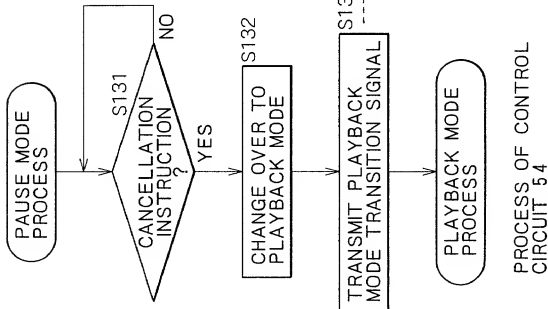


FIG. 16B

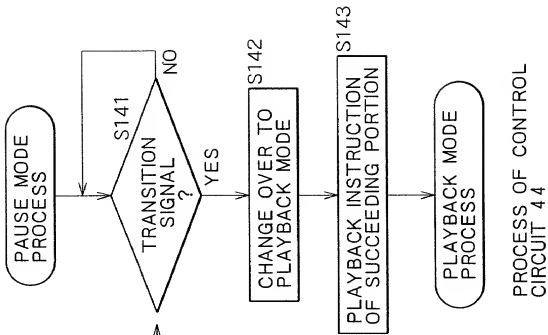


FIG. 17

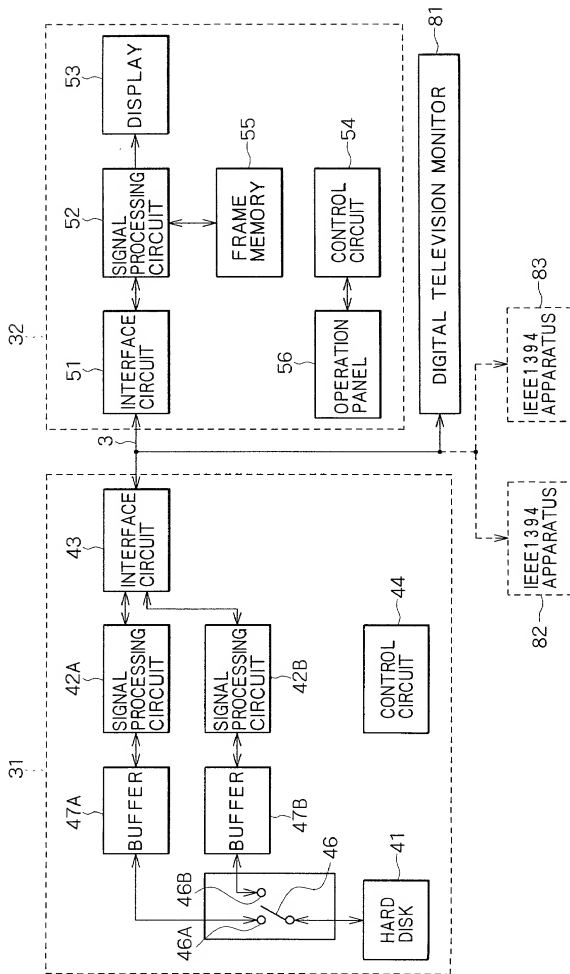


FIG. 18A

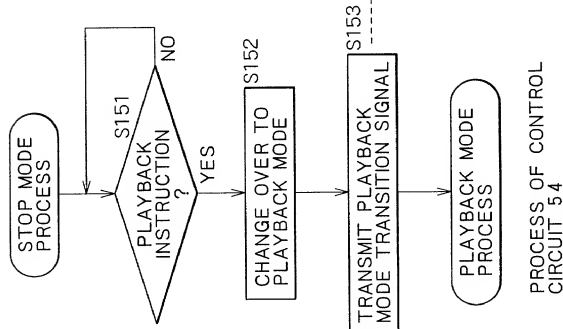


FIG. 18B

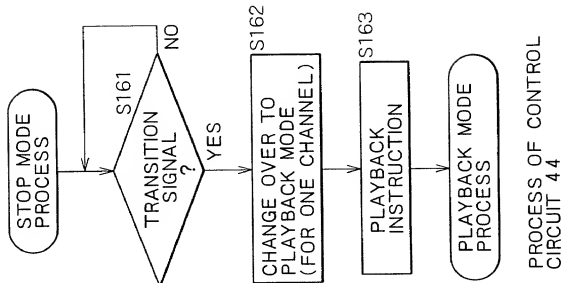


FIG. 19A

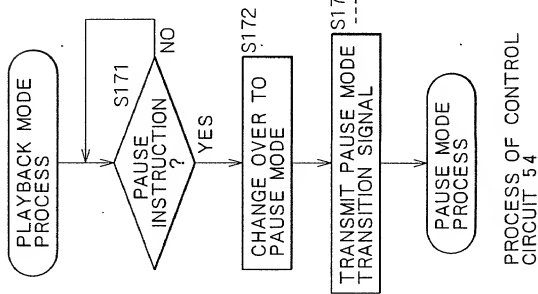


FIG. 19B

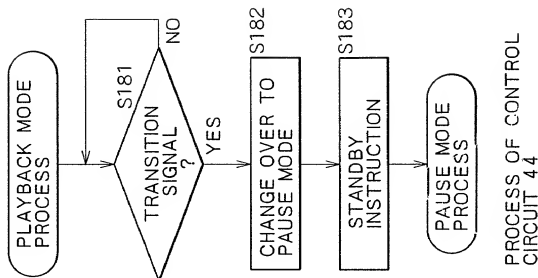


FIG. 20A

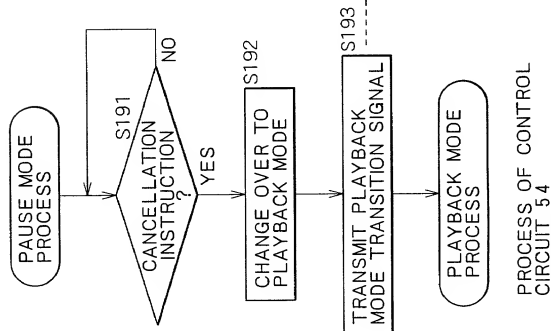


FIG. 20B

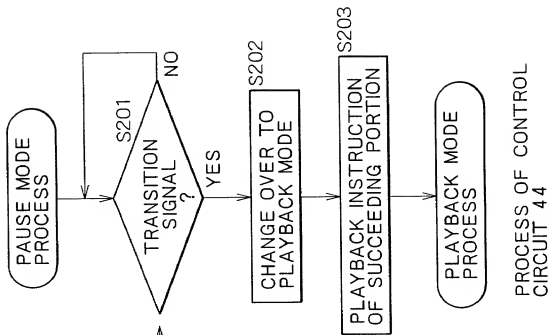


FIG.21

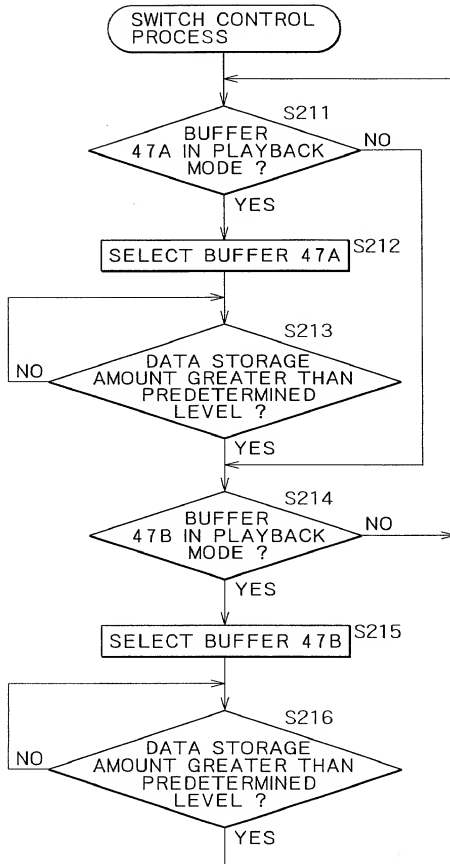


FIG. 22

